



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

काकतीय प्रौद्योगिकी एवं विज्ञान संस्थान, वरंगल - ५०६ ०१५ तेलंगाना, भारत

కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారతదేశము

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

PG – M.Tech. (EMBEDDED SYSTEM AND VLSI)

PRR - 20

SCHEME OF INSTRUCTION & EVALUATION

(I Semester to IV Semester)

(Applicable from the Academic Year 2021-22)

PROGRAM EDUCATIONAL OBJECTIVES (PEOs)	
PG – M.Tech. (EMBEDDED SYSTEM AND VLSI)	
PROGRAM EDUCATIONAL OBJECTIVES (PEOs)	The postgraduates of EMBEDDED SYSTEM AND VLSI will be able to ...
PEO1 (Research and Innovation)	<i>apply appropriate Electronic Design Automation tools to analyze & develop new methodologies to solve the real time problems in the domain of Embedded System & VLSI and contribute significantly in research and teaching</i>
PEO2 (Technical expertise and Successful career)	<i>excel as entrepreneurs and industrial professionals in the domain of Hardware & Software Development of Embedded Systems, RTOS, Embedded Firmware, ASIC Design & Verification, Advanced SoC and Semiconductor device modeling.</i>
PEO3 (Soft skills and Lifelong learning)	<i>exhibit professional ethics, effective communication and teamwork in solving engineering problems by adapting ancient scientific methodologies for sustainable development of society with an attitude of perpetual learning</i>

PROGRAM OUTCOMES (POs) & PROGRAM SPECIFIC OUTCOMES (PSOs)	
PG – M.Tech. (EMBEDDED SYSTEM AND VLSI)	
PROGRAM OUTCOMES (POs)	At the time of graduation, the postgraduates of EMBEDDED SYSTEM AND VLSI will be able to ...
PO1	<i>independently carry out research /investigation and development work to solve practical problems</i>
PO2	<i>to write and present an effective technical report/document</i>
PO3	<i>demonstrate competence in the area of Embedded System and VLSI</i>
PROGRAM SPECIFIC OUTCOMES (PSOs):	
PSO1	<i>apply knowledge of Embedded System and VLSI for development of effective and innovative solutions to engineering problems in the broad areas like Embedded System Design, VLSI Technology and applications</i>
PSO2	<i>utilize Electronic Design Automation tools to solve complex engineering problems in the domain of Embedded System and VLSI</i>

